N-Ch 30V Fast Switching MOSFETs

- ★ Super Low Gate Charge
- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

Product Summary

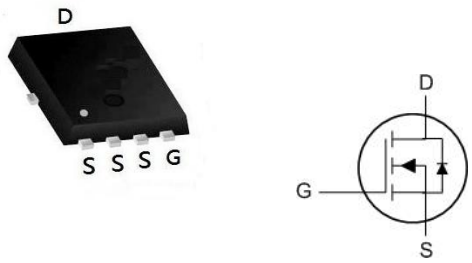
BVDSS	RDSON	ID
30V	3.5mΩ	100A

Description

The XXW100N03DF is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The XXW100N03DF meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

PDFN5X6 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating		Units
		10s	Steady State	
V_{DS}	Drain-Source Voltage	30		V
V_{GS}	Gate-Source Voltage	±20		V
I_D@T_C=25℃	Continuous Drain Current, V_{GS}@ 10V[1]	100		A
I_D@T_C=100℃	Continuous Drain Current, V_{GS}@ 10V[1]	50		A
I_D@T_A=25℃	Continuous Drain Current, V_{GS}@ 10V[1]	30	19	A
I_D@T_A=70℃	Continuous Drain Current, V_{GS}@ 10V[1]	25	16	A
I_{DM}	Pulsed Drain Current[2]	162		A
EAS	Single Pulse Avalanche Energy[3]	144.7		mJ
I_{AS}	Avalanche Current	53.8		A
P_D@T_C=25℃	Total Power Dissipation[4]	62.5		W
P_D@T_A=25℃	Total Power Dissipation[4]	6	2.42	W
T_{STG}	Storage Temperature Range	-55 to 175		℃
T_J	Operating Junction Temperature Range	-55 to 175		℃

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1]	---	62	℃/W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1] (t ≤10s)	---	25	℃/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	---	2.4	℃/W

Electrical Characteristics (T_J=25℃ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V,I_D=250μA	30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} =30V, V_{GS} = 0V,	-	-	1.0	μA
I_{GSS}	Gate to Body Leakage Current	V_{DS} =0V,V_{GS} = ±20V	-	-	±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}= V_{GS}, I_D=250μA	1.0	1.5	2.5	V
$R_{DS(on)}$	Static Drain-Source on-Resistance note3	V_{GS} =10V, I_D =30A	-	3.5	4.7	mΩ
		V_{GS} =4.5V, I_D =20A	-	7.0	10	
C_{iss}	Input Capacitance	V_{DS} =15V, V_{GS} =0V, f = 1.0MHz	-	2100	-	pF
C_{oss}	Output Capacitance		-	326	-	pF
C_{rss}	Reverse Transfer Capacitance		-	282	-	pF
Q_g	Total Gate Charge	V_{DS} =15V, I_D =30A, V_{GS} =10V	-	45	-	nC
Q_{gs}	Gate-Source Charge		-	3	-	nC
Q_{gd}	Gate-Drain(“Miller”) Charge		-	15	-	nC
$t_{d(on)}$	Turn-on Delay Time	V_{DS}=15V, I_D=30A, R_{GEN}=3Ω, V_{GS} =10V	-	21	-	ns
t_r	Turn-on Rise Time		-	32	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	59	-	ns
t_f	Turn-off Fall Time		-	34	-	ns
I_S	Maximum Continuous Drain to Source Diode Forward Current		-	-	50	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	200	A
V_{SD}	Drain to Source Diode Forward Voltage	V_{GS} = 0V, I_S=30A	-	-	1.2	V
trr	Body Diode Reverse Recovery Time	I_F=20A,dI/dt=100A/μs	-	15	-	ns
Qrr	Body Diode Reverse Recovery Charge		-	4	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition: T_J=25℃, V_G=10V, R_G=25Ω， L=0.5mH，I_{AS}=18.4A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

Figure1: Output Characteristics

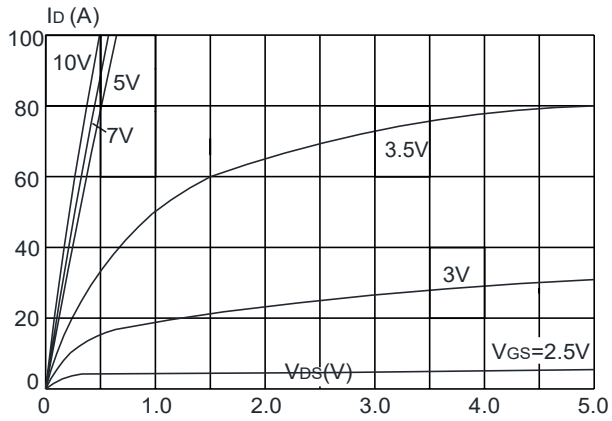


Figure 2: Typical Transfer Characteristics

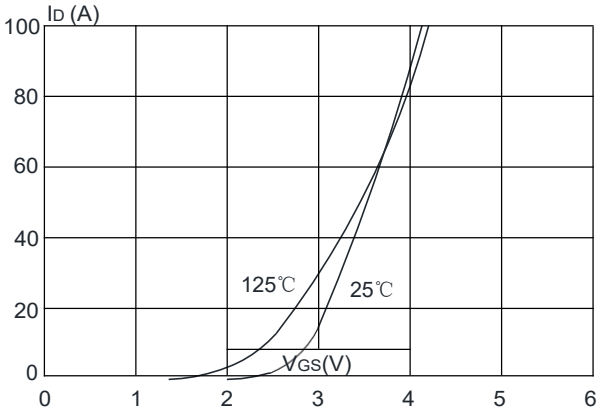


Figure 3: On-resistance vs. Drain Current

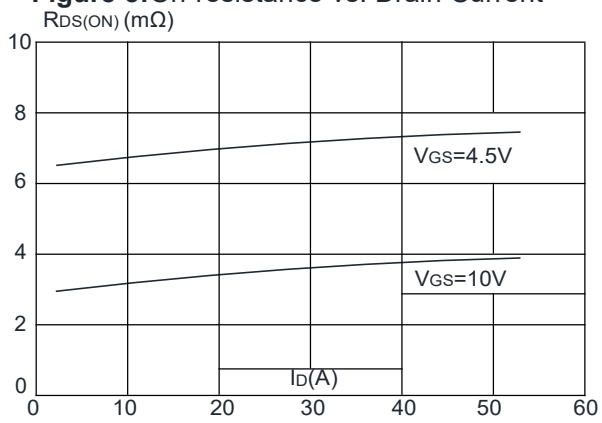


Figure 4: Body Diode Characteristics

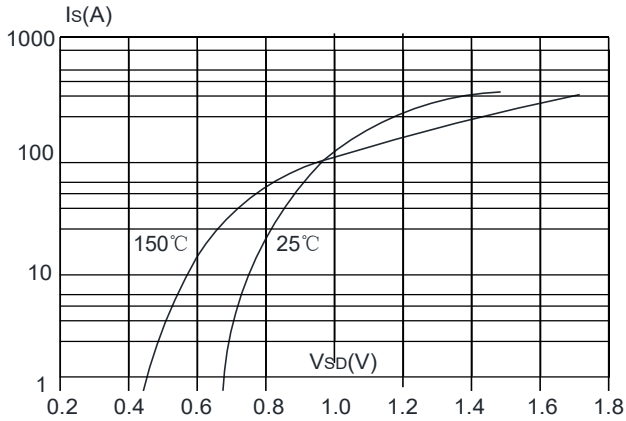


Figure 5: Gate Charge Characteristics

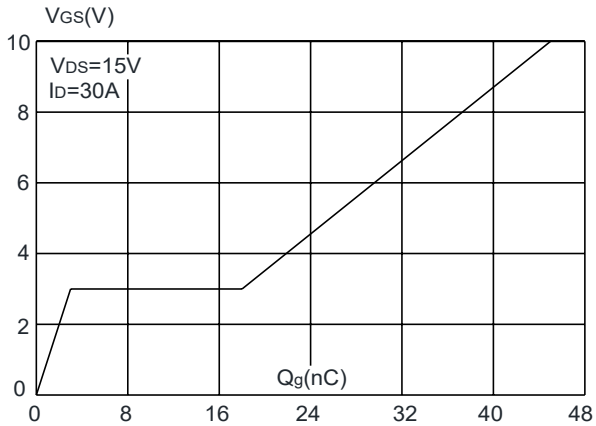


Figure 6: Capacitance Characteristics

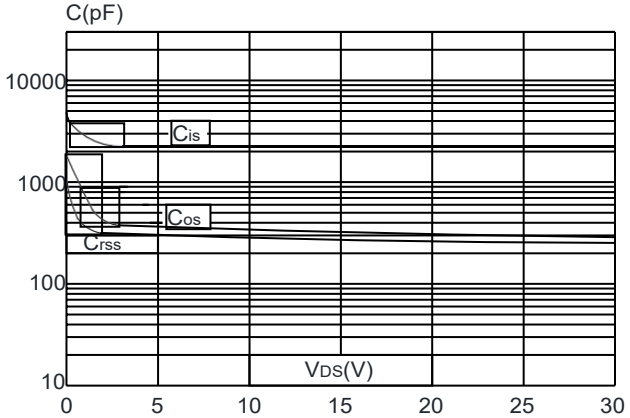


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

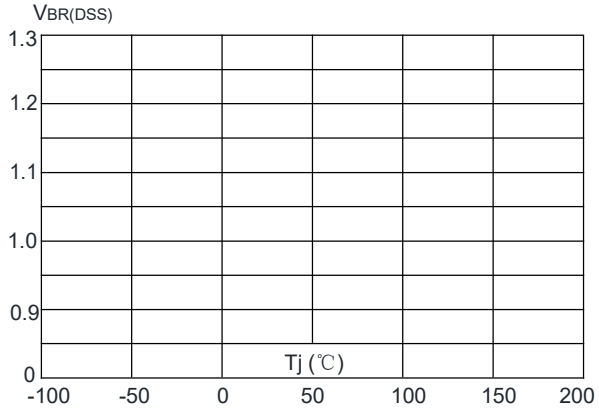


Figure 8: Normalized on Resistance vs. Junction Temperature

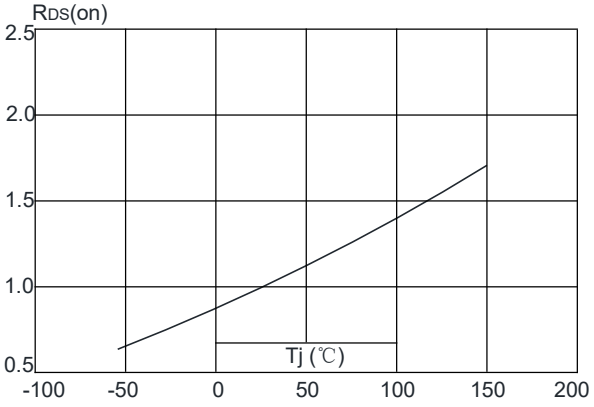


Figure 9: Maximum Safe Operating Area

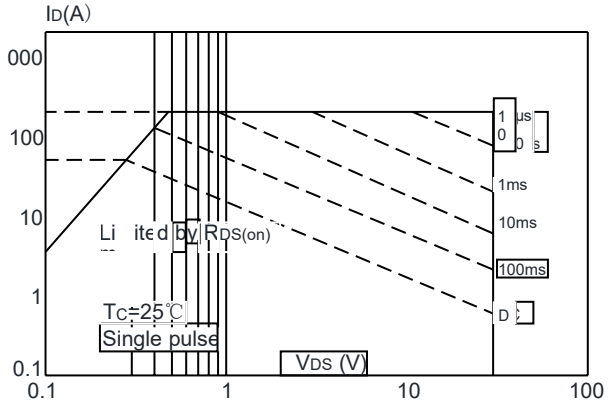


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

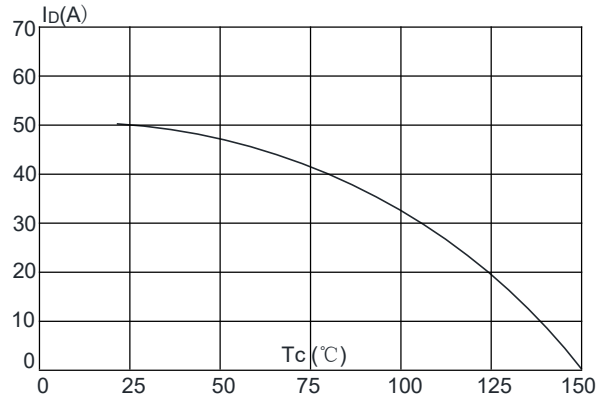
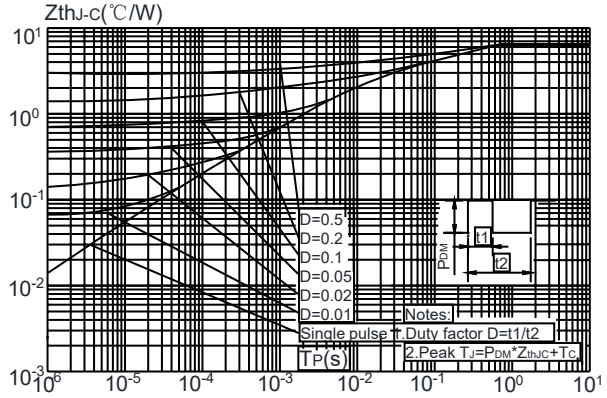


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case (PDFN3.3*3.3-8L)



Test Circuit

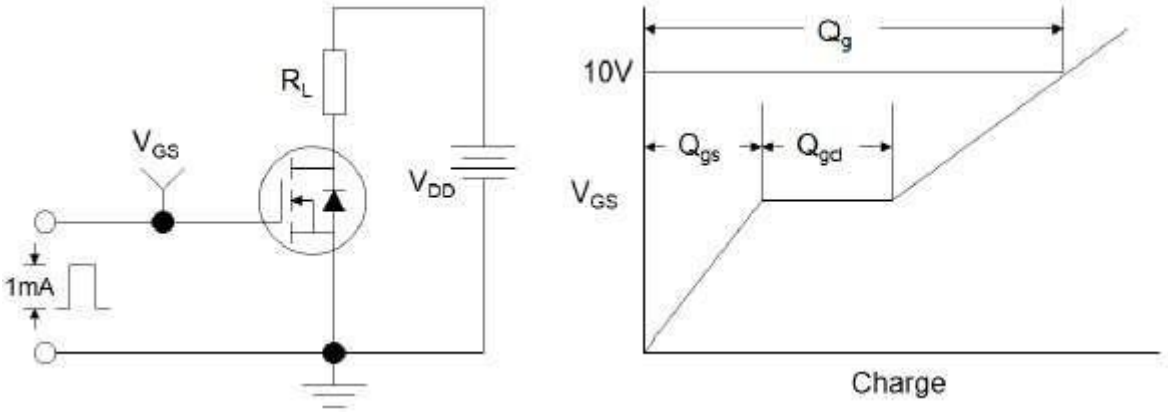


Figure1:Gate Charge Test Circuit & Waveform

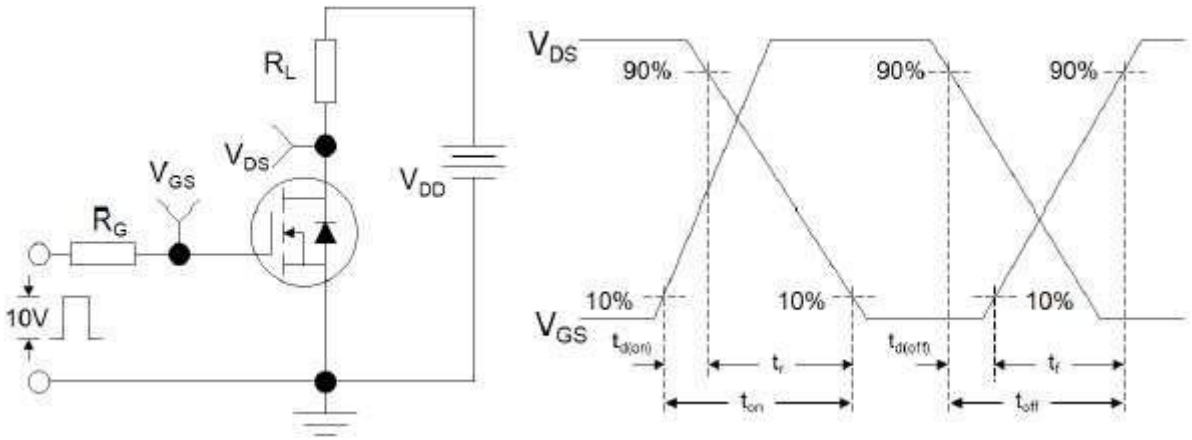


Figure 2: Resistive Switching Test Circuit & Waveforms

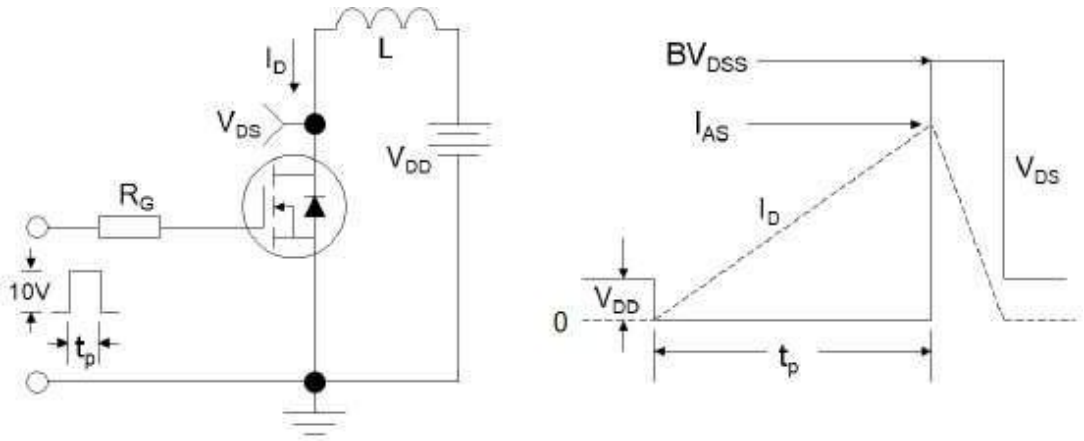
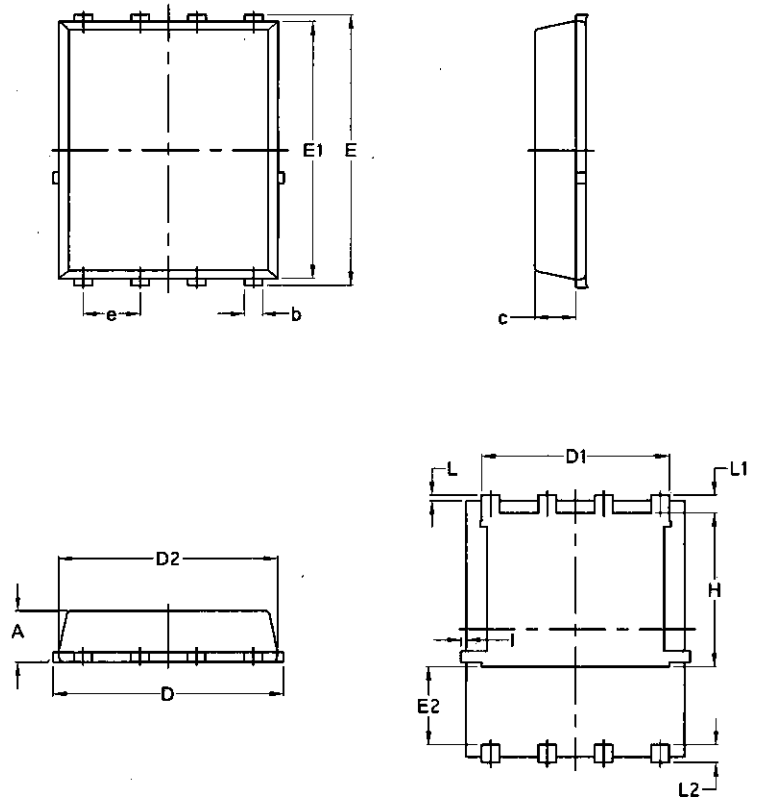


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

Package Mechanical Data-DFN5*6-8L-JQ Single



Symbol	Common			
	mm		Inch	
	Mim	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070